



अखिल भारतीय तकनीकी शिक्षा परिषद्

(भारत सरकार का एक सांविधिक निकाय)

(शिक्षा मंत्रालय, भारत सरकार)

नेल्सन मंडेला मार्ग, वसंत कुंज, नई दिल्ली-110070

**ALL INDIA COUNCIL FOR TECHNICAL EDUCATION**

(A Statutory Body of the Govt. of India)

(Ministry of Education, Govt. of India)

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**F. NO. 01/AICTE/TLB/ATAL/ARM EDU/2023**

**Date: 06.07.2023**

To,

All Vice Chancellors of State Technical Universities,  
Directors / Principals of AICTE approved technical institutions,

**Sub: Virtual Faculty Development Workshop- VLSI to System design: Silicon-to-end application approach from 31<sup>st</sup> July to 4<sup>th</sup> August 2023.**

Sir/ Madam,

All India Council for Technical Education (AICTE) in association with **Arm Education** and **ST Microelectronics** with support from **Cadence Design Systems** is launching a Faculty Development Workshop on “**VLSI to System Design: Silicon-to-End Application Approach**” from 31<sup>st</sup> July to 4<sup>th</sup> August 2023. Aim of this workshop is to give insights to the faculty members on the latest technologies and developments prevailing in semiconductor related domains. The 5-day virtual workshop will take you through the VLSI SoC silicon design steps to Industry 4.0 end-use applications.

Please circulate this among all the faculty members and research scholars of Electronics and Communication Engineering, Electrical and Electronics Engineering, VLSI and Embedded Systems and authorize at least two nominations from each institute for the workshop. It is to be noted that there shall be **No Registration Charges**. The participants will be issued with a Certificate on successful completion of course.

For more information on the workshop – Registration, Agenda, Speakers, etc., please visit the link <https://community.arm.com/vlsi-to-system-design/>

Please feel free to contact the mentioned below officials for any related queries

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Regards

Dr Ramesh Umikrishnan

Advisor

Training & Learning Bureau